Microprocessors and Microcontrollers

Course	19EE3602	Year	III	Semester	II
Code					
Course	Professional	Branch	EEE	Course Type	Theory
Category	Core				
Credits	3	L-T-P	3-0-0	Prerequisites	Digital
				_	Systems
Continuous		Semester		Total	
Internal	30	End	70	Marks:	100
Evaluation:		Evaluation:			

Course Outcomes							
Upon successful completion of the course, the student will be able to							
CO1	Have a clear understanding of the architecture and instruction set of 8086 and 8051.(L2)						
CO2	Develop 8086 and 8051 assembly language programs to perform a given task.(L3)						
CO3	Interface peripherals and memories with 8086 and 8051.(L4)						
CO4	Design real-time application of Microprocessors and Microcontrollers.(L6)						

Contribution of Course Outcomes towards achievement of Program Outcomes &														
Strength of correlations (H:High, M: Medium, L:Low)														
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3												1	
CO2	3	2		3	2								2	
CO3	3	3	2	2	3								3	
CO4	3	3	3	3	3								3	3
	Syllabus													
Unit	Contents									Ma	apped			
No.											CO			
Ι	Intel8086													
	Introduction and evolution of Microprocessors, Architecture of 8086, Register								ter (CO1				
	Organization of 8086, Memory Organization of 8086, Pin diagram of 8086.									.01				
	Minimum and Maximum mode operations of 8086, General Bus Operation of 8086,								86,					
	Read and Write cycle timing diagram.													
II	ASSE		Y LAN	GUA	E PR	OGRA	MMI	NG					C	201,
	Addressing Modes and Instruction set, Assembler Directives, Procedures and Macros,									os, (202			
	simple assembly language programming.													
III	Basic	Perip	herals	and In	terfaci	ing								
	Static Memory interfacing with 8086, 8255 PPI, Architecture of 8255 PPI, Various								ous C	203.				
	modes of operations and interface of I/O devices to 8086 using 8255, Interfacing A/D,							/D, C	204					
	D/A Converter, Stepper motor interface.													
	Programmable DMA Controller 8257, Programmable Interrupt Controller 8259,								59,					
	Serial Communication Interface USART 8251.													
IV	8051 Microcontrollers													
	Intel 8051 architecture, memory organization, flags, stack, and special function							ion C	201,					
	registers, I/O ports counters and timers, serial data I/O, interrupts. Addressing modes,								les, (CO2				
	instructions set, Simple assembly language Programming.													
V	Interfacing and Applications of 8051								C	203,				
	Inter	facing	extern	al me	mory,	Interfa	acing 8	8051 t	o LEI	D's, Re	elay's a	and La	tch (204
	Connections, interfacing seven segment display, ADC and DAC interfacing, Stepper								per					
	motor	contro	ol.	-		-	-	-				-	-	
	1													

Learning Resources

Text Books

- 1. Douglas V. Hall, "Microprocessors and Interfacing", TMH-Revised 2nd edition, 2006.
- 2. A. K. Ray and K. M. Burchandi, "Advanced Microprocessors and interfacing", Tata McGraw Hill, 2nd edition, 2006.
- 3. Kenneth J. Ayala, "The 8051 Microcontroller Architecture, Programming and Applications", Thomson Publishers, 2nd Edition, 2004

Reference Books

- 1. Ajay V. Deshmukh, "Microcontrollers Theory & Applications", Tata McGraw Hill, 2005.
- M.A. Mazidi, R.D. McKinlay, J.G. Mazidi, "The 8051 Microcontroller: A Systems Approach", Pearson, 2013.
- 3. Kenneth J Ayala, "The 8086 Microprocessors Architecture, Programming and Interfacing the PC", West Publishers, 1995.

e- Resources & other digital material

- 1. https://nptel.ac.in/courses/108/103/108103157/
- 2. <u>https://nptel.ac.in/courses/108/107/108107029/</u> (Web Content)
- 3. <u>https://nptel.ac.in/courses/108/105/108105102/</u>